



AKASH INSTITUTE OF ENGINEERING AND TECHNOLOGY

Affiliated to Visvesvaraya Technological University, Belagavi and Approved by AICTE, New Delhi.
Devanahalli, Bengaluru- 562110 www.akashiet.com



Mandatory Disclosure

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1. Name of the Institution

Akash Institute of Engineering and Technology

Prasannahalli Main Road, Devanahalli

Bengaluru Rural– 562110, Karnataka

Mobile No. : +91-9743873555

Email : akshtechnical23@gmail.com principal@akashiet.com

2. Name and address of the Trust/Society/Company and the Trustees

Akash Education and Development Trust

Ward No. 22, Prashanth Nagar, Devanahalli

Bengaluru Rural– 562110, Karnataka

3. Name and Address of the Principal

Dr. Prakash S Dabeer

Akash Institute of Engineering and Technology,

Prasannahalli Main Road, Devanahalli

Bengaluru Rural– 562110, Karnataka

Mobile No.: +91-9743873555

Email: principal@akashiet.com

4. Name of the Affiliating University

VISVESVARAYA TECHNOLOGICAL UNIVERSITY

Jnanasangama Campus, Machhe,

Belagavi -590 018, Karnataka.

5. Governance

5.1 Governing Council

List of Governing Council Members

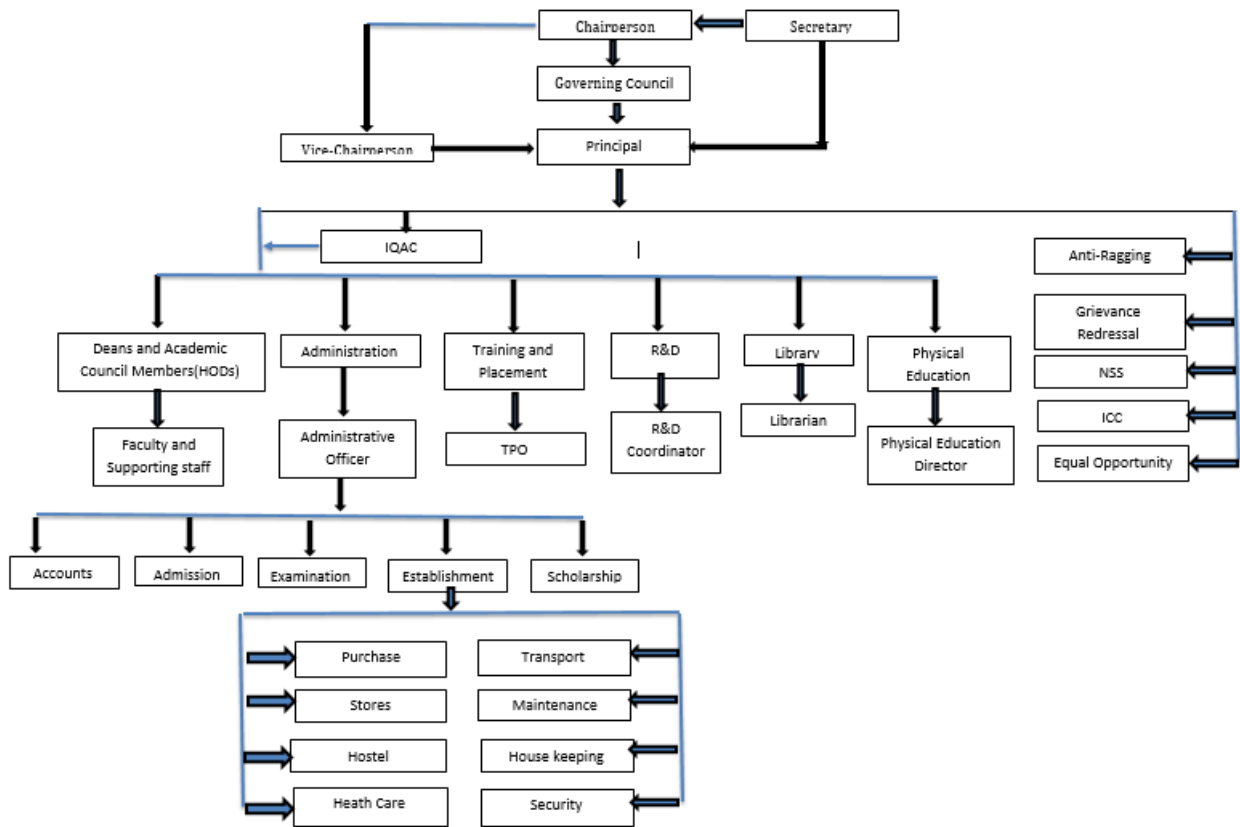
Sl. No.	Members as per AICTE	Name of the member
1	Chairperson	Smt. Pushpa Muniraju Chairperson, Akash Group of Institution Devanahalli, Bengaluru-562110
2	Secretary	Sri. K. Muniraju Secretary, Akash Group of Institution Devanahalli, Bengaluru-562110
3	Vice-Chairman	Sri. Amar Gowda Vice-Chairman, Akash Group of Institution Devanahalli, Bengaluru-562110
4	Member	Sri. Prabhakar H Ramdas Chief Finance Officer, Akash Group of Institution Devanahalli, Bengaluru-562110
5	Nominee of the Visvesvaraya Technological University, Belagavi	Nominee, Visvesvaraya Technological University, Belagavi
6	Nominee of the All India Council for Technical Education (AICTE)	R.O. / Nominee, AICTE-SWRO, Bengaluru
7	Nominee of the State Government	Director Directorate of Technical Education
8	Industrialist	Mr. Vijay Kumar S Makal Proprietor Essem Powder Coatings, Bengaluru and President, Bangalore Powder Coated Association
9	Member Secretary	Dr. Prakash S Dabeer Principal, Akash Institute of Engineering and Technology Devanahalli, Bengaluru-562110
10	Faculty member	Dr. Pranesh K G Associate Professor, Dept. of Mechanical Engineering Akash Institute of Engineering and Technology Devanahalli, Bengaluru-562110
11	Faculty member	Dr. Kavya H S Assistant Professor, Dept. of Mathematics Akash Institute of Engineering and Technology Devanahalli, Bengaluru-562110

5.2 Member of Academic Advisory Body


Akash Institute of Engineering and Technology is having an academic council with the following members.

Sl No.	Name of the Members	Designation	Qualification
1	Dr. Prakash S Dabeer Principal, Akash Institute of Engineering and Technology	Chairman	Ph.D.
2	Dr. Pranesh K G HOD- Mechanical Engineering	Member	Ph.D.
3	Mr. Madhu R HOD-Computer Science and Engineering	Member	M.Tech
4	Mrs. Pragathi M HOD-Information Science and Engineering	Member	M.Tech
5	Mrs. Manasa K HOD-Artificial Intelligence and Machine Learning	Member	M.Tech
6	Mr. Anil Kumar Warad HOD-Artificial Intelligence and Data Science	Member	M.Tech
7	Mr. Vijay Kumar Y M HOD-Computer Science and Engineering (Data Science)	Member	M.Tech
8	Mr. Nagamma HOD-Computer Science and Engineering (Cyber Security)	Member	M.Tech
9	Mrs. Deepti Prakash HOD-Electronics and Communication Engineering	Member	M.Tech
10	Dr. Santhosh T C M HOD- Physics	Member	Ph.D.
11	Dr. Kavya H S HOD-Mathematics	Member	Ph.D.
12	Dr. Shivareddy G V HOD-Chemistry	Member	Ph.D.

5.3 Organizational Chart and Processes



5.4 Establishment of Anti Ragging Committee –Yes



AKASH INSTITUTE OF ENGINEERING & TECHNOLOGY

(Unit of Akash Education & Development Trust)
Prasannahalli Main Road, Near International Airport, Devanahalli, Bengaluru - 562 110
Ph : +91 9743873555 | Email : akashtechical23@gmail.com | Web : www.akashiet.com

Ref. No. _____ Date: 05-11-2024

Office Order

This institute has constituted the Anti-ragging Core Committee with the following members. This order will come into effect from 07.11.2024 until further orders.

Sl No.	Name	Designation & Department	Role
1.	Dr. Prakash S Dabeer	Principal	Chairman
2.		Police Inspector, Devanahalli Town	Special Invitee
3.	Mr. Sunil Kumar B S	Assistant Professor (ECE)	Convenor
4.	Mr. Chetan	General Administrator, Department of Administration	Member
5.	Mr. Ibrahim	Director Physical Education & Sports.	Member
6.	Mrs. Manasa K	Assistant Professor (CSE)	Member
7.	Mr. Madhu R	HOD, Department of CSE	Member
8.	Mrs. Deepthi Prakash	HOD, Department of ECE	Member
9.	Mr. Prajwal Gowda	Chief Warden	Member
10.	Student Representatives: Mr. Arvind P (AIML 2 nd year) Ms. Shravanthi L (AI & DS 2 nd Year)	Boy Student Girl Student	Student Members

Functions:

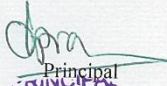
- Monitoring ragging preventions in and around the campus.
- Monitoring anti-drugs activities around the college campus.
- Ensure that the guidance from the regulatory bodies is followed to curb the menace of ragging in the institute campus and Hostels.

Terms:


- Two years an shall continue until further reconstitution

Meeting Frequency:

Every Semester and may be schedules as and when required.


 Principal
 AKASH INSTITUTE OF ENGINEERING & TECHNOLOGY
 Prasannahalli Main Road, Devanahalli
 Bengaluru - 562 110

5.5 Establishment of Online Grievance Redressal Mechanism – Yes



AKASH INSTITUTE OF ENGINEERING AND TECHNOLOGY
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 Prasanna Halli Main Road, Devanahalli, Bengaluru-562110

02/09/2024

OFFICE ORDER

STUDENT GRIEVANCE REDRESSAL COMMITTEE

The Students Grievance Redressal Committee(SGRC) Constituted in the College to address and resolve any issue/grievance raised by the students within the preview of the institution norms.

Sl. No.	Name	Designation and Department	Role	Contact Number	Email ID
01	Dr. Prakash S Dabeer	Principal	Chairman	9860676940	Principal@akashiet.com
02		VTU Nominee	Member		
03	Dr. Santhosh T C M	Assistant Professor	Member/ Convenor	7899250264	Santhosh@akashiet.com
04	Prof. Shravani M A	Assistant Professor	Member	8904136676	shravanianjaneyareddy@gmail.com
05	Dr. Shiva Reddy G V	Assistant Professor	Member	8310403107	Shivareddy.g.v@gmail.com
06	Prof. Sneha C	Assistant Professor	Member	8549932772	Snehacsneha97@gmail.com
07	Mr. Chetan N	Administrator	Member	8310434645	

Copy to:

1. All the faculty members of AIET
2. All HODs
3. HR Department
4. Office file

(Signature)
Principal
PRINCIPAL
AKASH INSTITUTE OF ENGINEERING & TECHNOLOGY
 Prasannahalli Main Road, Devanahalli
 Bengaluru - 562 110.

5.6 Establishment of Grievance Redressal Committee in the Institution and Appointment of OMBUDSMAN by the University – Yes (VTU SHALL appoint OMBUDSMAN)

5.7 Establishment of Internal Complaint Committee(ICC) – Yes



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Ref. No. AEPT/AIET/AICTE/ICC/2024-24/022

Date : 01/01/2024

OFFICE ORDER

Constitution of College Internal Complaint Committee

**Subject: Appointment of Constitution of College Internal Complaint Committee (CICC)
Faculty/Staff-Regards**

The Constitution of College Internal Complaint Committee (CICC) constituted in the college to address and resolve any issues/complaints raised by faculty, staff, students within the preview of the institution norms.

Sl no	Name	Designation and Department	Role	Mobile no& Email ID
1	Prof Kavya H S	Assistant Professor, Mathematics	Chairperson	8095551553 kavya@akashiet.com
2	Prof Deepthi Prakash	Assistant Professor, ECE	Member Secretary	8050161658 deepthiprakash@akashiet.com
3	Prof Shashikanth	Assistant Professor, Mechanical	Member	9611174173 shashikanth@akashiet.com
4	Miss. Latha	Establishment Section	Member	9731239489 latha@akashiet.com
5	Mr. Chethan	Administrator	Member	8310434645 chethan@akashiet.com
7	Mr. Mahesh	NGO Representative	Member	990090999
8	Miss. Rakshitha R	Student, CSE	Member	8660084900 rakshitharacchu422@gmail.com
9	Mr. Kushal H C	Student, AI&DS	Member	7899219965 hckushal21@gmail.com
10	Miss. Amrutha G Kumar	Student, AI&DS	Member	9620673847 amruthagirishkumar2005@gmail.com

FUNCTIONS:

- Prevent discrimination and sexual harassment, by promoting gender amity among students and employees;
- Deal with cases of discrimination and sexual harassment against women, in a time bound manner, aiming at ensuring support services to the victimized and termination of the harassment;
- To provide conducive environment and congenial atmosphere in the campus;
- Maintain confidentiality in all aspects of any proceedings of the Committee;
- Recommend appropriate redressal and punitive action against the guilty to the Management;

Copy to:

All Members of Committee, HOD's, HR Department, Office file.


AKASH INSTITUTE OF ENGINEERING & TECHNOLOGY
Prasannahalli Main Road, Devanahalli,
Bengaluru - 562 110

5.8 Internal Quality Assurance Cell – Yes**AKASH INSTITUTE OF ENGINEERING AND TECHNOLOGY**

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Devanahalli, Bengaluru- 562110, www.akashiet.com


01/09/2024

Internal Quality Assurance Cell (IQAC)**IQAC Core Committee**

Sl No	Name of the Faculty	Designation	Role
1	Dr. Prakash S Dabeer	Principal	Chairperson
2	Mr. Amar Gowda	Vice Chairman Akash Group of Institution	Management Representative- Member
3	Dr. Pranesh K G	Assistant Professor and HOD-Mechanical Engineering	Coordinator
4	Prof. Anil Kumar Ward	Assistant Professor and HOD-Artificial Intelligence and Data Science	Member
5	Dr. Kavya H S	Assistant Professor and HOD-Mathematics First Year Coordinator - Chemistry cycle	Member
6	Prof. Sneha C	Assistant Professor Dept. of Civil Engineering	Member
7	Ms. Bhavya C M 3 rd Semester-CSE	Student Representative	Member

Copy to

1. Administrative officer, AIET
2. All HODs, AIET
3. All Faculty members, AIET


 Dr. Prakash S Dabeer

AKASH INSTITUTE OF ENGINEERING & TECHNOLOGY
Prasannaiahli Main Road, Devanahalli
Bengaluru - 562 110.

6. Programmes

6.1 Name of Programmes approved by AICTE 2024-25

Sl No.	Level	Name of the Programme	Course	Intake
1	Under Graduate	Engineering and Technology	Artificial Intelligence (AI) and Data Science	60
2	Under Graduate	Engineering and Technology	Computer Science & Engineering	60
3	Under Graduate	Engineering and Technology	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	60
4	Under Graduate	Engineering and Technology	Computer Science and Engineering (Cyber Security)	30
5	Under Graduate	Engineering and Technology	Computer Science and Engineering (Data Science)	30
6	Under Graduate	Engineering and Technology	Electronics and Communication Engineering	30
7	Under Graduate	Engineering and Technology	Information Science and Engineering	60
8	Under Graduate	Engineering and Technology	Mechanical Engineering	30
9	Post Graduate	Management	MBA	60
10	Post Graduate	Computer Applications	MCA	60

6.2 Total number of Courses

Under Graduate :08

Post Graduate :02

6.3 Programme: Name/ number of seat/duration

SI No.	Name of the Programme	Number of Seats	Duration
1	Artificial Intelligence (AI) and Data Science	60	4
2	Computer Science & Engineering	60	4
3	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	60	4
4	Computer Science and Engineering (Cyber Security)	30	4
5	Computer Science and Engineering (Data Science)	30	4
6	Electronics and Communication Engineering	30	4
7	Information Science and Engineering	60	4
8	Mechanical Engineering	30	4
9	MBA	60	2
10	MCA	60	2

6.5 Fee (as approved by the state government)

Under Graduation: Tuition fee Rs.1,07,495/- + college fee Rs. 20,000/-+ University fee Rs. 10,610/-

Post Graduate: Tuition fee Rs. 69,310 /- + college fee Rs. 20,000/-+ University fee Rs. 10,610/-

7. Faculty

7.1 Course/Branch wise list Faculty members LIST

Sl.No	Name of the faculty	Designation	Department
1	Dr. Pranesh K G	Associate Professor	Mechanical Engineering
2	Dr. Shashikanth G S	Assistant Professor	Mechanical Engineering
3	Mr. Mahesh Mokshith M L	Assistant Professor	Mechanical Engineering
4	Mr. Madhu R	Assistant Professor	Computer Science & Engineering
5	Mrs. K R Soujanya	Assistant Professor	Computer Science & Engineering
6	Mrs. Meghana K L	Assistant Professor	Computer Science & Engineering
7	Mrs. Pragathi M	Assistant Professor	Information Science and Engineering
8	Mrs. Pallavi A	Assistant Professor	Information Science and Engineering
9	Mrs. Manasa K	Assistant Professor	Computer Science and Engineering (Artificial Intelligence and Machine Learning)
10	Mrs. Jyothi	Assistant Professor	Computer Science and Engineering (Artificial Intelligence and Machine Learning)
11	Mr. Anil Kumar Warad	Assistant Professor	Artificial Intelligence (AI) and Data Science
12	Mr. Praveen P	Assistant Professor	Artificial Intelligence (AI) and Data Science
13	Mrs. Nagamma	Assistant Professor	Computer Science and Engineering (Cyber Security)
14	Mr. Vijay Kumar Y M	Assistant Professor	Computer Science and Engineering (Data Science)

15	Mrs. Deepthi Prakash	Assistant Professor	Electronics and Communication Engineering
16	Mr. B S Sunil Kumar	Assistant Professor	Electronics and Communication Engineering
17	Mrs. Pranitha	Assistant Professor	Electronics and Communication Engineering
18	Dr. Santhosh T C M	Assistant Professor	Physics
19	Mrs. Chaitrashree J N	Assistant Professor	Physics
20	Dr. Kavya H S	Assistant Professor	Mathematics
21	Dr. Praveen Kumar M	Assistant Professor	Mathematics
22	Mr. Pramod Gowda	Assistant Professor	Mathematics
23	Ms. Shravani M A	Assistant Professor	Mathematics
24	Mrs. Mamatha S	Assistant Professor	Mathematics
25	Dr. Shiva Reddy	Assistant Professor	Chemistry
26	Mr. Hemantha T	Assistant Professor	Chemistry
27	Mrs. Sneha C	Assistant Professor	Civil Engineering
28	Dr. Sri Hari	Associate Professor	MBA
29	Mrs. Varsha T R	Assistant Professor	MBA
30	Ms. Archana	Assistant Professor	MBA
31	Ms Teena Kumari	Assistant Professor	MBA
32	Mr. Madhu N	Assistant Professor	MCA
33	Ms. Priyanka	Assistant Professor	MCA
34	Mr. Ibrahim M	Physical Education Director	Physical Education
35	Mr. Shankara M N	Librarian	Library

7.2 Permanent Faculty – 35

7.3 Permanent Faculty: Student Ratio: 1:16

8. Profile of Principal

Name: Dr. Prakash S Dabeer

Date of Birth:18-08-1966

Education Qualifications: BE, ME, Ph.D.

Work Experience

Teaching: 33years

Research:10 years

Administration:21 Years (As a Principal:11 years and as a HOD:10 years)

Industry :01year

Area of Specialization: Optimisation Techniques and Design of Experiments

Courses taught at Under Graduate/ Post Graduate Level: Mechanical Engineering subjects

Research guidance (Number of Students):02

No. of papers published in National/International Journals/Conferences:65

Projects Carried out: AICTE-MODROB (Modernization of Computer Integrated Manufacturing (CIM) Lab)

9. Admission

9.1 Number of seats sanctioned with the year of approval

Sl.No	Department	Sanctioned intake	Year of Establishment	Approved up to
1	Artificial Intelligence (AI) and Data Science	60	2023-24	2024-25
2	Computer Science & Engineering	60	2023-24	2024-25
3	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	60	2023-24	2024-25
4	Computer Science and Engineering (Cyber Security)	30	2023-24	2024-25
5	Computer Science and Engineering (Data Science)	30	2023-24	2024-25
6	Electronics and Communication Engineering	30	2023-24	2024-25
7	Information Science and Engineering	60	2023-24	2024-25
8	Mechanical Engineering	30	2023-24	2024-25
9	MBA	60	2023-24	2024-25
10	MCA	60	2023-24	2024-25

9.2 Admission UG and PG :AY 2024-25

Sl. No.	Level	Name of the Programme	Course	Intake	Admitted
1	Under Graduate	Engineering and Technology	Artificial Intelligence (AI) and Data Science	60	44
2	Under Graduate	Engineering and Technology	Computer Science & Engineering	60	37
3	Under Graduate	Engineering and Technology	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	60	51
4	Under Graduate	Engineering and Technology	Computer Science and Engineering (Cyber Security)	30	15
5	Under Graduate	Engineering and Technology	Computer Science and Engineering (Data Science)	30	14
6	Under Graduate	Engineering and Technology	Electronics and Communication Engineering	30	19
7	Under Graduate	Engineering and Technology	Information Science and Engineering	60	26
8	Under Graduate	Engineering and Technology	Mechanical Engineering	30	0
9	Post Graduate	Management	MBA	60	16
10	Post Graduate	Computer Applications	MCA	60	22

9.3 Number of applications received during last year for admission under Management Quota and number admitted AY:2024-25

Sl. No.	Level	Name of the Programme	Course	Application Received	Admitted
1	Under Graduate	Engineering and Technology	Artificial Intelligence (AI) and Data Science	41	34
2	Under Graduate	Engineering and Technology	Computer Science & Engineering	26	23
3	Under Graduate	Engineering and Technology	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	48	37
4	Under Graduate	Engineering and Technology	Computer Science and Engineering (Cyber Security)	18	11
5	Under Graduate	Engineering and Technology	Computer Science and Engineering (Data Science)	13	9
6	Under Graduate	Engineering and Technology	Electronics and Communication Engineering	19	9
7	Under Graduate	Engineering and Technology	Information Science and Engineering	17	5
8	Under Graduate	Engineering and Technology	Mechanical Engineering	5	0
9	Post Graduate	Management	MBA	6	2
10	Post Graduate	Computer Applications	MCA	12	8

10. Admission Procedure

10.1 Mention the admission test being followed, name and address of the Test Agency/State Admission Authorities and its URL (website)

CET - Karnataka Examination Authority, Sampige Road, Malleshwaram, Bangalore- 560012

<https://cetonline.karnataka.gov.in/kea/>

COMED K - # 132, Second Floor, 11th main, 17th cross, Malleshwaram, Bangalore 560055

<https://www.comedk.org/>

PGCET - KEA, Sampige Road, Malleshwaram, Bangalore- 560012

<https://cetonline.karnataka.gov.in/kea/>

**10.2 Number of seats allotted to different Test Qualified candidate separately (AIEEE//JEE/ CET (State conducted test/ University tests/ CMAT)/ Association conducted test etc.)
AY:2024-25**

Sl. No.	Level	Name of the Programme	Course	CET	COMED K
1	Under Graduate	Engineering and Technology	Artificial Intelligence (AI) and Data Science	10	0
2	Under Graduate	Engineering and Technology	Computer Science & Engineering	14	0
3	Under Graduate	Engineering and Technology	Computer Science and Engineering (Artificial Intelligence and Machine Learning)	13	1
4	Under Graduate	Engineering and Technology	Computer Science and Engineering (Cyber Security)	4	0
5	Under Graduate	Engineering and Technology	Computer Science and Engineering (Data Science)	5	0
6	Under Graduate	Engineering and Technology	Electronics and Communication Engineering	10	0
7	Under Graduate	Engineering and Technology	Information Science and Engineering	21	0
8	Under Graduate	Engineering and Technology	Mechanical Engineering	0	0
9	Post Graduate	Management	MBA	14	NA
10	Post Graduate	Computer Applications	MCA	14	NA

10.3 Calendar for admission against Management/vacant seats:

- The Institute follow the calendar in line with KEA and COMED K
- Open date of request for applications - 28.03.2024
- Last date for closing of admission –As per Admission authority of the State.
- Starting of the Academic session - As per VTU calendar of Events
- The waiting list shall be activated only on the expiry of date of main list - yes
- The policy of refund the Fee, in case of withdrawal, shall be clearly notified - yes

11. Criteria and Weightages for Admission

Passed second PUC/12th standard /equivalent examination with English as one of the languages and obtained a minimum of 45% of marks in aggregate in physics and mathematics along with chemistry/Biology/Bio-Technology / Electronics/Computer

12. List of Applicants

12.1 List of candidate whose applications have been received along with percentile/percentages core for each of the qualifying examination in separate categories for open seats: Maintained

12.2 List of candidate who have applied along with percentage and percentile score for Management quota seats (merit wise): Maintained

13. Information of Infrastructure and Other Resources Available**13.1 Number of Class Rooms and size of each:**

Number of class rooms: 30 Size of each class room: 82.3 Sq.m

13.2 Number of Tutorial rooms and size of each

Number of Tutorial rooms:03 Size of each Tutorial room: 33 Sq.m

13.3 Number of Laboratories and size of each

Number of Laboratories:16 Size of each Laboratory: 66 Sq.m

13.4 Number of Computer Centres with capacity of each:

Number of computer centres: 01 Each lab capacity: 150

13.5 Central Examination Facility, Number of rooms and capacity of each:

Central Examination Facility: Available

Number of Rooms:30 Capacity of each room:28

13.6 Barrier Free Built Environment for disabled and elderly persons: Yes

13.7 Hostel Facilities: Yes

13.8 Number of Library books/e-books/Titles/Journals available

- Total Titles: 427
- Total Volumes: 2873
- Journals: 6
- Magazines:1

13.9 List of online National/International Journals subscribed: VTU consortium

13.10 National Digital Library (NDL) subscription: Yes

13.11 List of Major Equipment/Facilities in each Laboratory/Workshop**For I & II year B.E.**

Sl. No.	Name of the Laboratory/Workshop	Major Equipments Available
1	Engineering Physics Lab	Function Generator, Four Probe Kit, Semiconductor Laser , Digital Travelling Microscope, Magnetic stirrer.
2	Engineering Chemistry Lab	Conductivity meter, Potentiometer, Colorimeter, Flame Photometer, pH Meter
3	Workshop	Lathe machine, Milling Machine, Bench Grinder, Power Hacksaw, Welding, Bench Vice, Hand Tools, Surface plate.
4	CAED Lab	30 Computers with Solid edge V20 Software Printers
5	Computer labs	570 Computers with Printing facility along with softwares such as Turbo c ++, Anaconda, Python, Code blocks, Solid Edge, Microsoft Office, Google Chrome, Oracle, Xilinx, Adobe reader, Java Exclipse, Lab view, Oracle Virtualbox (Fedora)
6	Analog and Digital System Design Lab	CROs, Function Generators, Regulated power supplies, Digital IC trainer kit, Transformers, Multimeters
7	Communication Lab	CROs, Function Generators, Dual Power supplies, DSOs, Multimeters
8	EC software lab	30 computers with softwares such as Matlab, LABVIEW/XILINX/Pspice
9	Language lab	20 Computers

13.12 List of Experimental Setup in each Laboratory/Workshop

Sl. No.	Name of the Laboratory/Workshop	List of Experimental Setup
1	Engineering Physics Lab	<ul style="list-style-type: none"> • Series LCR Resonance Circuits • Charging and Discharging of Capacitor • Planck's Constant • Young's Modulus by Single Cantilever • Transistor Characteristics • Wavelength of LASER using Diffraction Grating • Parallel LCR Resonance Circuit • Black Box Experiment • PHET Interactive Simulation • Magnetic Field along the Axis of the Coil
2	Engineering Chemistry Lab	<ul style="list-style-type: none"> • Conductometric estimation of acid mixture • Potentiometric estimation of FAS using $K_2Cr_2O_7$ • Determination of pKa of vinegar using pH sensor (Glass electrode) • Estimation of total hardness of water by EDTA method • Estimation of Copper present in electroplating effluent by optical sensor (colorimetry) • Determination of Viscosity coefficient of lubricant (Ostwald's viscometer) • Determination of Chemical Oxygen Demand (COD) of industrial waste water sample • Estimation of Sodium present in soil/effluent sample using flame photometry • Synthesis of Iron-oxide Nanoparticles (Common to all branches) • Electrolysis of water (CSE Stream) • Determination of strength of an acid in Pb-acid battery (C S E & E C Stream)

3	Analog and Digital System Design Lab	<ul style="list-style-type: none"> • Design and set up the BJT common emitter voltage amplifier with and without feedback and determine the gain- bandwidth product, input and output impedances. • Design and set-up BJT/FET i) Colpitts Oscillator, ii) Crystal Oscillator • Design and set up the circuits using opamp: i) Adder, ii) Integrator, iii) Differentiator and iv) Comparator • Design 4-bit R – 2R Op-Amp Digital to Analog Converter (i) for a 4-bit binary input using toggle switches(ii) by generating digital inputs using mod-16 • Design and implement (a) Half Adder & Full Adder using basic gates and NAND gates, (b) Half subtractor & Full subtractor using NAND gates, (c) 4-variable function using IC74151(8:1MUX). • Realize (i) Binary to Gray code conversion & vice-versa (IC74139), (ii) BCD to Excess-3 code conversion • a) Realize using NAND Gates: i) Master-Slave JK Flip-Flop, ii) D Flip-Flop and iii) T Flip-Flop b) Realize the shift registers using IC7474/7495: (i) SISO (ii) SIPO (iii) PISO (iv) PIPO (v) Ring counter and (vi) Johnson counter. • Realize a) Design Mod – N Synchronous Up Counter & Down Counter using 7476 JK Flip-flop b) Mod-N Counter using IC7490 / 7476 c) Synchronous counter using IC74192
	LabVIEW	<ul style="list-style-type: none"> • Basic arithmetic operations: addition, subtraction, multiplication and division • Boolean operations: AND, OR, XOR, NOT and NAND • Sum of ‘n’ numbers using ‘for’ loop • Factorial of a given number using ‘for’ loop • Determine square of a given number

		<ul style="list-style-type: none"> • Factorial of a given number using ‘while’ loop • Sorting even numbers using ‘while’ loop in an array • Finding the array maximum and array minimum
	Verilog -XILINX	<ul style="list-style-type: none"> • To simplify the given Boolean expressions and realize using Verilog program • To realize Adder/Subtractor (Full/half) circuits using Verilog data flow description. • To realize 4-bit ALU using Verilog program. • To realize the following Code converters using Verilog Behavioral description a) Gray to binary and vice versa b) Binary to excess3 and vice versa • To realize using Verilog Behavioral description: 8:1 mux, 8:3 encoder, Priority encoder • To realize using Verilog Behavioral description: 1:8 Demux, 3:8 decoder, 2-bit Comparator • To realize using Verilog Behavioral description: • Flip-flops: a) JK type b) SR type c) T type and d) D type • To realize Counters-up/down (BCD and binary) using Verilog Behavioural description.
	PSPICE	<ul style="list-style-type: none"> • Design and Test (i) Bridge Rectifier with Capacitor Input Filter ii) Zener voltage regulator • Design and Test Biased Clippers-a) Positive, b) Negative, c) Positive-Negative d) Positive and Negative Clampers with and without Reference • Plot the transfer and drain characteristics of a JFET and calculate its drain resistance, mutual conductance and amplification factor

		<ul style="list-style-type: none"> • Plot the transfer and drain characteristics of n-channel MOSFET and calculate its parameters, namely; drain resistance, mutual conductance and amplification factor. • Design and test Emitter Follower • Design and plot the frequency response of Common Source JFET/MOSFET amplifier • Test the Op amp Comparator with zero and non-zero reference and obtain the hysteresis curve • Design and test Full wave Controlled rectifier using RC triggering circuit • Design and test Precision Half wave and full wave rectifiers using Op amp • Design and test RC phase shift oscillator
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13.13 Innovation Cell: Yes

13.14 Social Media Cell: Yes

13.15 To upload the respective short video (1-2 min) of Infrastructure and facilities available w.r.t the courses in the website: Yes

13.16 Games and Sports Facilities

- **Indoor Sports facilities:** Table Tennis, Chess, Carom
- **Outdoor Sports facilities:** Football (Lawn Ground), Volleyball (1 courts), Basketball (two courts), Tennis (two courts), Kabaddi, Swimming pool, Pavilion

13.17 Extra-Curricular Activities: Cultural Cell available

13.18 Teaching Learning Process

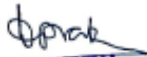
- Curriculum and syllabus for each of the Programmes as approved by the University www.vtu.ac.in
- Academic Calendar of the University – as per VTU.
- Academic Time Table: Available
- Teaching Load of each Faculty: As per Standard Norms
- Internal Continuous Evaluation System: Yes
- Student's Assessment of Faculty, System in place: Yes

13.19 For each Post Graduate Courses give the following

Title of the Course: MBA and MCA

Laboratory facilities exclusive to the Post Graduate Course: 01

13.20 MoUs with Industries: Yes


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